

TRITON™

TRITON™ is a complete computer, implemented on a board smaller than a credit card, and ready to be designed into your embedded system. TRITON™ includes a 400MHz Intel® XScale™ processor, SDRAM and Flash memory. The integrated LCD-controller enables direct connection of a LCD screen, and the standard-PCMCIA-interface permits simple extension and integration into a target system. Made as a standard-DIMM144-module, TRITON™ occupies an area of just 67.6 x 36.6 mm with a maximum height of 8.5 mm.

XScale™

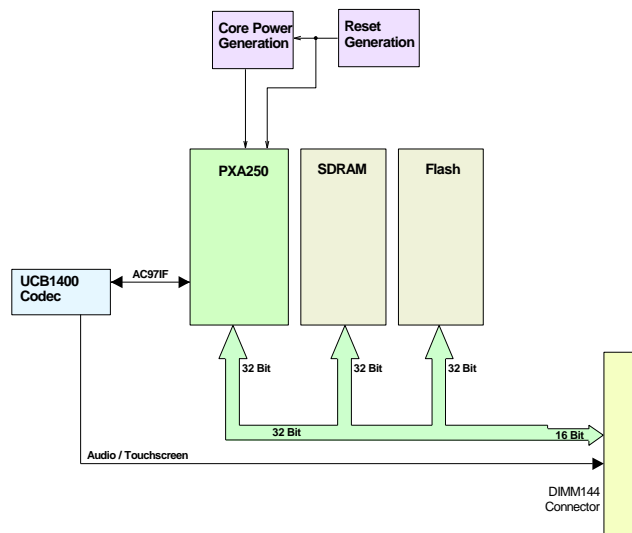
Intel's new XScale™ processor family increases efficiency and decreases processor power consumption. The Intel® XScale™ microarchitecture is based on the solid and widely used foundation of the Intel® StrongARM technology. Optimized for the development of highly efficient mobile internet devices, and for network infrastructure applications, Intel® StrongARM and Intel® XScale™ are compatible with the ARM architecture, which in turn guarantees the compatibility of software solutions. Turbo mode enables the processor to scale its performance as high or as low as necessary in a single clock cycle, which helps conserve battery lifetime while still meeting performance requirements. In addition, the new micro-power management features for these devices allow the processors to potentially use less than half the power at the same performance levels of the Intel StrongARM SA-1110 applications processor.

BitCtrl® IPL und Startup

TRITON™ is delivered with pre-installed BitCtrl® IPL firmware. BitCtrl® IPL and QNX supports several low-level-debugging options and file download via serial XModem or TFTP via ethernet. These files can additionally be stored into the permanent flash-memory to be started by command or power-on.

TRITON™ features:

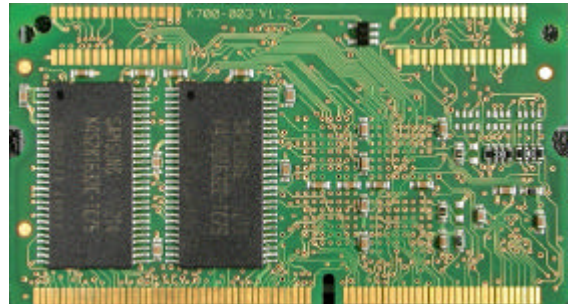
- Intel XScale™ PXA250 (400 MHz)
- 64 MByte SDRAM (32-bit@100Mhz)
- 32 Mbyte Flash memory (32-bit)
- Reset logic, I²C
- 3 asynchronous, 1 synchronous serial interfaces
- LCD controller
- PC-CARD / compact-flash interface
- JTAG interface
- Single 3,3V power supply
- Core-voltage generation with more than 90% efficiency
- DIMM144-module, 67,6 x 36,6 mm
- UCB 1400 20-bit Stereo-Audio-Codec and touchscreen interface
- BitCtrl® IPL firmware, Startup and QNX® Neutrino



top view



rear view

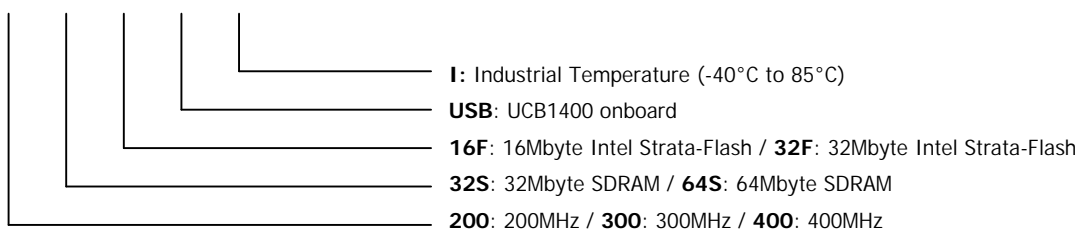


Ordering information

Order Number (Valid Combination)	PXA250	USB1400	SDRAM	Flash
TRITON/400/64S/32F/UCB	400MHz	x	64MB	32MB

The order number is formed by a combination of the elements below. Other Valid Combinations planned to be supported in volume. Consult the sales office to confirm availability of specific combinations and to check on newly released combinations.

TRITON / 400 / 64S / 32F / UCB / I



DIMM144 connector pinout

Pin	Signal	Description	Pin	Signal	Description
1	TSMY	Touch Screen, neg Y-connector	73	LDD5 / GPIO63	LCD interface data bus
2	TSMX	Touch Screen, neg X-connector	74	LDD2 / GPIO60	LCD interface data bus
3	TSPY	Touch Screen, pos Y-connector	75	LDD3 / GPIO61	LCD interface data bus
4	TSPX	Touch Screen, pos X-connector	76	LDD0 / GPIO58	LCD interface data bus
5	MICP	Microfon Input (see UCB 1400)	77	LDD1 / GPIO59	LCD interface data bus
6	FF_RI / GPIO38	Full Function UART Ring Indicator	78	GND	Ground
7	MICGND	Microfon GND – Signal (see UCB 1400)	79	PWE# / GPIO49	PCMCIA Interface Write Enable
8	LINE_IN_R	Line_In – right channel (see UCB 1400)	80	POE# / GPIO48	PCMCIA Interface Output Enable
9	LINE_OUT_R	Line_Out – right channel (see UCB 1400)	81	PIOW# / GPIO51	PCMCIA Interface I/O Write
10	LINE_IN_L	Line_In – left channel (see UCB 1400)	82	PIOR# / GPIO50	PCMCIA Interface I/O Read
11	VREFDRV	reference voltage for head phone driver	83	PWAIT# / GPIO56	PCMCIA Interface Wait
12	LINE_OUT_L	Line_Out – left channel (see UCB 1400)	84	PIOIS16# / GPIO57	PCMCIA Interface I/O select 16 Bit
13	AD3	Analog Input 3 (see UCB 1400)	85	PREG# / GPIO55	PCMCIA Interface Register Select
14	AD2	Analog Input 2 (see UCB 1400)	86	PSKTSEL / GPIO54	PCMCIA Interface Socket Select
15	AD1	Analog Input 1 (see UCB 1400)	87	PCE1# / GPIO52	PCMCIA Interface Low Byte Enable
16	AD0	Analog Input 0 (see UCB 1400)	88	PCE2# / GPIO53	PCMCIA Interface High Byte Enable
17	GND	GND	89	+3,3V	power supply
18	GND	GND	90	+3,3V	power supply
19	TMS	JTAG Test Mode Select	91	D14	memory data bus
20	TCK	JTAG Test Clock	92	D15	memory data bus
21	TRST#	JTAG Test Reset	93	D12	memory data bus
22	TDO	JTAG Test Data Out	94	D13	memory data bus
23	RESET_INPUT#	Reset Input	95	D10	memory data bus
24	TDI	JTAG Test Data In	96	D11	memory data bus
25	RESET_OUT#	Reset Output	97	D8	memory data bus
26	L_BIAS / GPIO77	LCD bias drive	98	D9	memory data bus
27	BT_RxD / GPIO42	Bluetooth UART Receive Pin (3,3V-Level)	99	D6	memory data bus
28	BATT_FAULT	Battery Fault, switch into sleepmode	100	D7	memory data bus
29	BT_TxD / GPIO43	Bluetooth UART Transmit Pin (3,3V-Level)	101	D4	memory data bus
30	IR_RXD / GPIO46	IrDA Receive Pin (3,3V- Level)	102	D5	memory data bus
31	FF_RxD / GPIO34	Full Function UART Peceive (3,3V-Level)	103	D2	memory data bus
32	IR_TxD / GPIO47	IrDA Transmit Pin (3,3V-Level)	104	D3	memory data bus
33	FF_TxD / GPIO39	Full Function UART Transmit (3,3V-Level)	105	D0	memory data bus
34	USB_N	USB-Port neg. Pin (3,3V-Level)	106	D1	memory data bus
35	+3,3V	power supply	107	GND	Ground
36	+3,3V	power supply	108	GND	Ground
37	SDA	I2C data signal	109	RDY / GPIO18	Ready Pin (Wait)
38	USB_P	USB-Port pos. Pin (3,3V-Level)	110	WE#	Memory Write Enable
39	SSP_TxD / GPIO25	Synchronous Serial Port Transmit Pin	111	RD/WR#	Read not Write
40	SCL	I2C Clock Signal	112	OE#	Memory Output Enable
41	SSP_CLK / GPIO23	Synchronous Serial Port Clock Pin	113	GND	Ground
42	SSP_FRM / GPIO24	Synchronous Serial Port Frame Pin	114	CS5# / GPIO33	Chip Select
43	DREQ0 / GPIO20	DMA Request Channel 0	115	CS4# / GPIO80	Chip Select
44	SSP_RxD / GPIO26	Synchronous Serial Port Receive Pin	116	CS3# / GPIO79	Chip Select
45	FF_DCD / GPIO36	Full Function UART Carrier Detect (3,3V)	117	CS2# / GPIO78	Chip Select
46	DREQ1 / GPIO19	DMA Request Channel 1	118	CS1# / GPIO15	Chip Select
47	FF_DTR / GPIO40	Full Function UART Data Term. Rdy. (3,3V)	119	A25	Memory address bus
48	FF_DSR / GPIO37	Full Function UART Data Set Rdy. (3,3V)	120	A24	Memory address bus
49	FF_RTS / GPIO41	Full Function UART Rdy. To Send (3,3V)	121	A23	Memory address bus
50	FF_CTS / GPIO35	Full Function UART Clear To Send (3,3V)	122	A22	Memory address bus
51	BT_RTS / GPIO45	Bluetooth UART Ready To Send Pin (3,3V)	123	A21	Memory address bus
52	BT_CTS / GPIO44	Bluetooth UART Clear To Send Pin (3,3V)	124	A20	Memory address bus
53	GPIO10	General Purpose I/O-Pin	125	A19	Memory address bus
54	GPIO11	General Purpose I/O-Pin	126	A18	Memory address bus
55	LDD14 / GPIO72	LCD interface data bus	127	A17	Memory address bus
56	LDD15 / GPIO73	LCD interface data bus	128	A16	Memory address bus
57	LDD12 / GPIO70	LCD interface data bus	129	A15	Memory address bus
58	LDD13 / GPIO71	LCD interface data bus	130	A14	Memory address bus
59	LDD10 / GPIO68	LCD interface data bus	131	A13	Memory address bus
60	LDD11 / GPIO69	LCD interface data bus	132	A12	Memory address bus
61	LDD8 / GPIO66	LCD interface data bus	133	A11	Memory address bus
62	LDD9 / GPIO67	LCD interface data bus	134	A10	Memory address bus
63	GPIO0	General Purpose I/O-Pin	135	A9	Memory address bus
64	GPIO1	General Purpose I/O-Pin	136	A8	Memory address bus
65	GND	Ground	137	A7	Memory address bus
66	GND	Ground	138	A6	Memory address bus
67	L_FCLK / GPIO74	LCD Interface Frame Clock	139	A5	Memory address bus
68	L_LCLK / GPIO75	LCD Interface Line Clock	140	A4	Memory address bus
69	L_PCLK / GPIO76	LCD Interface Pixel Clock	141	A3	Memory address bus
70	LDD6 / GPIO64	LCD interface data bus	142	A2	Memory address bus
71	LDD7 / GPIO65	LCD interface data bus	143	A1	Memory address bus
72	LDD4 / GPIO62	LCD interface data bus	144	A0	Memory address bus

Memory-Map

In the Intel PXA250 Developer's Manual you will find the memory map of the PXA250 processor on pages 2-28 and 2-29. The TRITON-ETN uses the memory as follows:

```

0xA000 0000 - 0xA3FF FFFF  64 MBytes SDRAM

0x01FC 0000 - 0x01FF FFFF  256 kBytes reserved flash area (FIS directory)
0x01F8 0000 - 0x01FB FFFF  256 kBytes reserved flash area (RedBoot config)
0x0004 0000 - 0x01F7 FFFF  31,5 MBytes flash area available
0x0000 0000 - 0x0003 FFFF  256 kBytes reserved flash area (RedBoot)

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Note

The flash memory has an erase block size of 256 kBytes.

Internally used GPIO pins

The following PXA250 GPIO pins are internally used by the TRITON-ETN module:

GPIO number	direction	Used for ...
22	I	UCB1400 interrupt